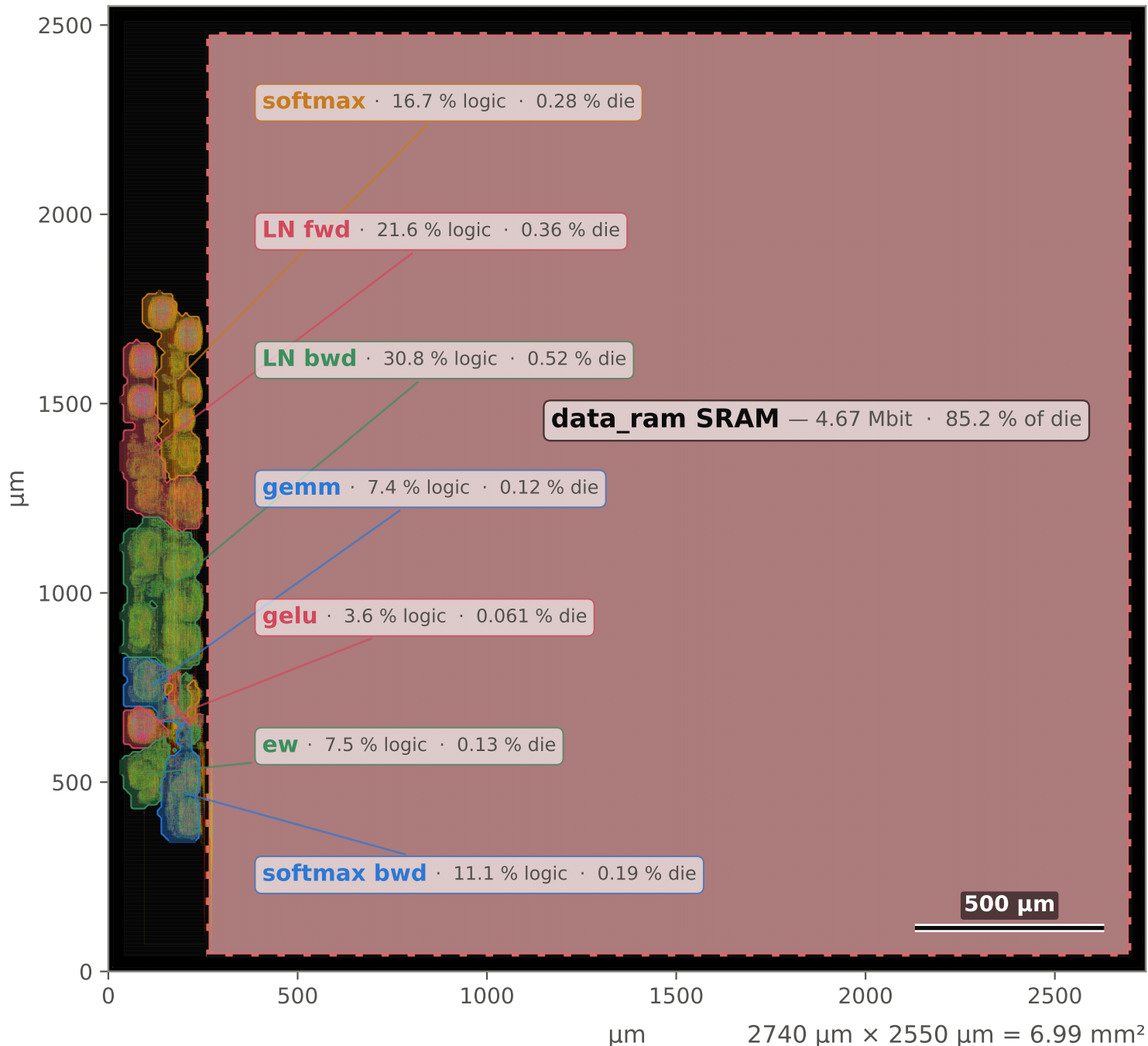


# v0 decoder trainer — routed die (gpdk045, 45 nm)

SRAM macro  $\approx$  85 % of die · datapath units in the west channel



colored fill = each 10  $\mu\text{m}$  tile's dominant unit (by placed-cell count); units interleave

key: black dash = SRAM macro · background = routed layout (power grid hidden)